

PTO-1449	Application No. 10/028,009	Applicant(s) Paul E. Doucette, et al.	
	Docket Number 004578.1147	Group Art Unit 2821	Filing Date December 21, 2001

**Information Disclosure Citation
In an Application**

U.S. PATENT DOCUMENTS

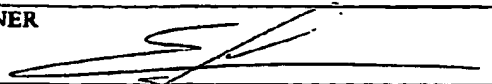
	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A	5,475,392	12/12/95	Newberg, et al.			
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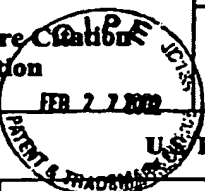
R	Guirong, et al. "Mixer-free All Digital Quadrature Demodulation", IEEE Proceedings of the Fourth International Conference on Signal Processing (ICSP), October 12, 1998, Vol. 2, pages 1704-1707 and an index sheet.
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EXAMINER 	DATE CONSIDERED 4/6/05
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PTO-1449 Information Disclosure Citations In an Application		Application No. 10/028,009 Docket Number 004578.1147		Applicant(s) Paul E. Doucette, et al. Group Art Unit Unknown		Filing Date December 21, 2001	
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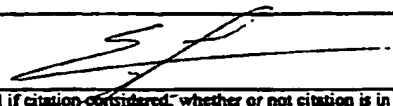
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A	4,839,652	06/13/89	O'Donnell, et al.			
B	4,983,970	01/08/91	O'Donnell, et al.			
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	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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R	U.S. Serial No. 09/567,543, filed May 5, 2000, entitled "PHASED ARRAY ANTENNA DATA RE-ALIGNMENT", by inventors G. Van Andrews, et al., 22 pages of text and 4 drawing sheets. (Attorney Docket No.004578.1005, 99E016).
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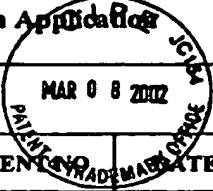
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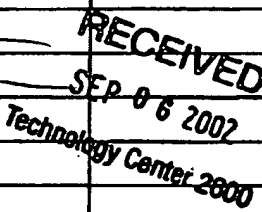
PTO-1449		Application No. 10/028,009		Applicant(s) Paul E. Doucette, et al.		
Information Disclosure Citation In an Application		Docket Number 004578.1147		Group Art Unit Unknown		
				Filing Date December 21, 2001		
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A	3,887,918	06/03/1975	Bailey			
B	4,688,045	08/18/1987	Knudsen			
C	4,749,995	06/07/1988	Hopwood, et al.			
D	4,886,069	12/12/1989	O'Donnell			
E	5,084,708	1/28/1992	Champeau, et al.			
F	5,223,843	06/29/1993	Hutchinson			
G	5,271,034	12/14/1993	Abaunza			
H	5,414,433	05/09/1995	Chang			
I	5,461,389	10/24/1995	Dean			
J	5,764,187	06/09/1998	Rudish, et al.			
K	5,943,010	08/24/1999	Rudish, et al.			
L	6,031,485	02/29/2000	Cellai, et al.			
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	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES NO
M	2 130 798 A	06/06/1984	UK			--
N	2 313 711 A	12/03/1997	UK			--
O	WO-01/67548 A2	09/13/2001	PCT			--
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DOCUMENT (Including Author, Title, Source, Pertinent Pages, and Date)						
R	Thompson, Rick et al., "An 8-Bit 2-Gigasample/Second A/D Converter Multichip Module for Digital Receiver Demonstration on Navy AN/APS-145 E2-C Airborne Early Warning Aircraft Radar", IEEE Trans on CPMT Part B, vol. 21, No. 4, 11/1998, pp. 447-462.					
S	Brinegar, C., et al. "Design of an Integrated RF Filter for the Direct Digitization Front End of a Dual GPS/GLONASS Software Radio Receiver", IEEE RAWCON '98 Proceedings, 8/1998, cover sheet and pp. 277-280.					
T	Ho, K.C., et al., "A Digital Quadrature Demodulation System", IEEE Trans. on AES, Vol. 32, No. 4, 10/1996, pp. 1218-1227.					
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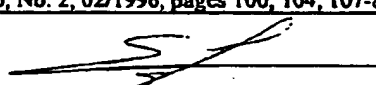


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		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	U	6,052,085	04/18/2000	Hanson, et al.			
	V	6,141,371	10/31/2000	Holmes, et al.			
	W	6,191,735	02/20/2001	Schineller			
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	GG						YES NO
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	LL Brown, A. et al., "Digital Downconversion Test Results with a Broadband L-Band GPS Receiver", IEEE Digital Avionics Systems Conf. 10/1994, cover sheet and pp. 426-431.
	MM Eerola, V. et al., "Direct Conversion Using Lowpass Sigma-Delta Modulation", Proc. IEEE Int'l Symposium on Circuits and Systems, 5/1992, cover sheet and pp. 2653-2656.
	NN Steyskal, Hans, "Digital Beamforming at Rome Laboratory", Microwave Journal, U.S. Horizon House, Dedham, vol. 176, No. 2, 02/1996, pages 100, 104, 107-8, 110, 113-4, 116, 119-120, 122,-124, 126.

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	CCC				
DOCUMENT (Including Author, Title, Source, Pertinent Pages, and Date)					
	DDD	U.S. Serial No. 09/519,069, filed March 3, 2000, entitled "DIGITAL PHASED ARRAY ARCHITECTURE AND ASSOCIATED METHOD", by inventor Gary A Frazier, text pages 1-31 and 3 drawing sheets. (Attorney Docket RAYT:009, case 37323).			
	EEE	Wong, A.C.C., "Radar Digital Beamforming", Standard Telecommunication Laboratories, Harlow, Essex, U.K., Military Microwaves Conference 1982, 10/20/83, cover sheet and pp. 287-294.			
	FFF	Caputi, William J., "Stretch: A Time-Transformation Technique", IEEE Transactions on Aerospace and Electronic Systems, Vol. AES-7, No. 2, March 1971, pp. 269-278.			
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